

AMENDMENTS TO THE CLAIMS

Please replace the claims, including all prior versions, with the listing of claims found below.

Listing of Claims:

1-14. (canceled)

15. (Previously presented) A circuit comprising:

a first switching element coupled to a first node and a second node;

a second switching element coupled to the first node;

a third switching element coupled to the second node;

a first capacitor coupled between the second switching element and a ground or reference voltage; and

a second capacitor coupled between the third switching element and the ground or reference voltage.

16. (Previously presented) The circuit of claim 15, further comprising:

a first clock signal to switch the first switching element between high and low; and

a second clock signal to switch the second and third switching elements between high and low.

17. (Previously presented) The circuit of claim 16, wherein the second clock signal is the phase-shifted complementary signal of the first clock signal.

18-25. (canceled)

26. (Previously presented) A method of nulling a charge, comprising:

switching a first switching element to off by turning a first clock signal to low, causing a charge injection charges and a clock feed-through charges to flow into a first node and a second node; and

switching a second switching element and a third switching element to on by turning a second clock signal to high, nulling the charge injection charges and clock feed-through charges as a result of absorbing the charge injection charges into a first capacitor and a second capacitor and generating a compensation signal with opposite polarities. respectively.

27-28. (canceled)